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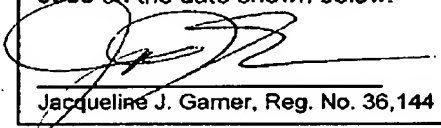
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFFICIAL

Appl. No.: 10/001,472 Confirmation No. 1913
Applicant: Robert H. Havemann
Filed: 11/01/2001
TC/A.U.: 2815
Examiner: Edward J. Wojciechowicz
Docket No.: TI-27506
Customer No.: 23494
Title: INTEGRATED CIRCUIT HAVING A DOPED POROUS DIELECTRIC
AND METHOD OF MANUFACTURING THE SAME

CERTIFICATION OF FACSIMILE TRANSMISSION

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FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (7 Pages)
<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT 1mth (3 Pages)
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<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Havemann	
TITLE OF INVENTION: Integrated Circuit Having Doped Porous . . .	
TI FILE NO.: TI-27506	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1/20/2004 DUE: 12/23/2003 ATTY/SEC'Y: JIG	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/001,472 Filing Date: 11/01/2001 Conf. No. 1913	

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